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CONFIRMATION NO. 6723

SERIAL NUMBER 10/717,370	FILING DATE 11/19/2003 RULE	CLASS 117	GROUP ART UNIT 1765	ATTORNEY DOCKET NO. 62603 CON2
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APPLICANTS

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** CONTINUING DATA *****

This application is a CON of 10/647,061 08/22/2003 PAT 6,830,964
which is a CIP of 10/603,696 06/26/2003 ABN
and is a CIP of 10/603,621 06/26/2003

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **

** 03/03/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	MA	DRAWING 9	28	2
Verified and Acknowledged	Examiner's Signature Initials				

ADDRESS

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TITLE

Method for making semiconductor device including band-engineered superlattice

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of

FILING FEE FEES: Authority has been given in Paper